CDA 3103 Computer Organization Module 6 Assignment

Part I: Problems

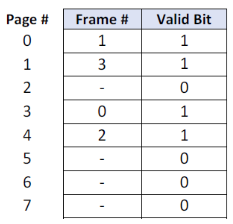
1. [26 points] Suppose a computer has bytes of byte-addressable main memory and a cache size of bytes, and each cache block contains 64 bytes.
2. [2 points] How many blocks of main memory are there? ( )
3. [2 points] How many blocks of cache memory are there? ( )
4. [6 points] If this cache is direct-mapped, what is the format of a memory address as seen by the cache; that is, the size of the tag field , the size of block field , and the size of the offset field .
5. [4 points] If this cache is fully associative, what is the format of a memory address as seen by the cache; that is, the size of the tag field , and the size of the offset field .
6. [6 points] If this cache is 4-way set associative, what is the format of a memory address as seen by the cache; that is, the size of the tag field , the size of set field , and the size of the offset field .
7. [6 points] If this cache is 16-way set associative, what is the format of a memory address as seen by the cache; that is, the size of the tag field , the size of set field , and the size of the offset field .
8. [30 points] Suppose we have a memory and a direct-mapped cache with the following characteristics.

* Memory is byte addressable
* Memory addresses are 16 bits
* The cache has 8 blocks
* Each cache block holds 16 bytes of data

1. [6 points] In the spaces below, indicate how the 16 address bits are allocated to the offset, index, and tag parts of the address used to reference the cache: \_\_\_\_\_\_\_\_ offset bits \_\_\_\_\_\_\_\_ tag bits \_\_\_\_\_\_\_\_ index bits.
2. [24 points] Below is a sequence of four binary memory addresses in the order they are used to reference memory. Assume that the cache is initially empty. For each reference, write down the tag and index bits and circle either hit or miss to indicate whether that reference is a hit or a miss.

|  |  |  |  |
| --- | --- | --- | --- |
| Memory address | Tag | Index | Hit / Miss (circle) |
| 0010 1101 1011 0011 |  |  | Hit Miss |
| 0000 0110 1111 1100 |  |  | Hit Miss |
| 0010 1101 1011 1000 |  |  | Hit Miss |
| 1010 1010 1010 1011 |  |  | Hit Miss |

1. [10 points] Suppose the cache access time is 15ns, main memory access time is 220ns, and the cache hit rate is 95%. Assuming non-overlapped access, what is the average access time for the processor to access an item?
2. [34 points] Suppose you have a byte-addressable virtual address memory system with 8 virtual pages of 64 bytes each, and 4 page frames. Assuming the following page table, answer the questions below:



1. [2 points] How many bits are in a virtual address?
2. [2 points] How many bits are in a physical address?
3. [2 points] How many bits are virtual page number?
4. [2 points] How many bits are page frame number?
5. [2 points] How many bits are page offset?
6. [24 points] What physical address corresponds to the following virtual addresses (if the address causes a page fault, simply indicate this is the case)?
   1. 0x00
   2. 0x44
   3. 0xC2
   4. 0x80

Part II: Submission Requirements

The following requirements are for electronic submission via Canvas.

* Your solutions must be in a single file with a file name yourname-module6-assignment.
* Upload the file by following the link where you download the homework description on Canvas.
* If scanned from hand-written copies, then the writing must be legible, or loss of credits may occur.
* Only submissions via the link on Canvas where this description is downloaded are graded. Submissions to any other locations on Canvas will be ignored.